

\$40	( read_n / write )	ID ID	\$08 \$D4	MSX2+ FS-A1 Series 1chipMSX / Zemmix Neo	Z80B 5.37MHz OCM-PLD v2.4 or later					
\$41	( read_n / write )	BIT 0		Smart Command ID	CPL or \$FF (Null)					
		BIT 1								
		BIT 2								
		BIT 3								
		BIT 4								
		BIT 5								
		BIT 6								
\$42	( read / write_n )	BIT 7								
		BIT 0				CPU Clock	Virtual DIP-SW1			
		BIT 1				Video Output (MSB)	Virtual DIP-SW2			
		BIT 2				Video Output (LSB)	Virtual DIP-SW3			
		BIT 3				Cartridge Slot-1	Virtual DIP-SW4			
		BIT 4				Cartridge Slot-2 (MSB)	Virtual DIP-SW5			
		BIT 5				Cartridge Slot-2 (LSB)	Virtual DIP-SW6			
\$43	( read / write_n )	BIT 6		Lock Mak for Toggles						
		BIT 7				Current Mapper Size	Virtual DIP-SW7			
		BIT 0				Current MegaSD Mode	Virtual DIP-SW8			
		BIT 1					CPU Clock			
		BIT 2					Video Output			
		BIT 3					Audio Mixer & CMT			
		BIT 4					Cartridge Slot-1			
BIT 5	Cartridge Slot-2									
BIT 6	Hard Reset Key									
\$44	( read / write_n )	BIT 7		Lights I/O	Internal Mapper					
		BIT 0			Internal MegaSD					
		BIT 1			Led 1 Status					
		BIT 2			Led 2 Status					
		BIT 3			Led 3 Status					
		BIT 4			Led 4 Status					
		BIT 5			Led 5 Status					
\$45	( read / write_n )	BIT 6		PSG Volume Level	Led 6 Status					
		BIT 7			Led 7 Status					
		BIT 0			Led 8 Status					
		BIT 1			BIT 0 (LSB)					
		BIT 2			BIT 2 (MSB)					
		\$46			( read / write_n )	BIT 3		PSG Mute	Status	
						BIT 4			BIT 0 (LSB)	
BIT 5	BIT 1									
BIT 6	BIT 2 (MSB)									
BIT 7	Status									
\$47	( read / write_n )		BIT 0			OPLL Volume Level			BIT 0 (LSB)	
			BIT 1						BIT 1	
		BIT 2	BIT 2 (MSB)							
		BIT 3	Status							
		BIT 4	BIT 0 (LSB)							
		\$48	( read / write_n )		BIT 5			SCC-I Volume Level	BIT 1	
					BIT 6				BIT 2 (MSB)	
BIT 7	Status									
BIT 0	BIT 0 (LSB)									
BIT 1	BIT 1									
\$49	( read / write_n )			BIT 2		OPLL Mute			BIT 2 (MSB)	
				BIT 3					Status	
		BIT 4	BIT 0 (LSB)							
		BIT 5	BIT 1							
		BIT 6	BIT 2 (MSB)							
		\$4A	( read / write_n )	BIT 7				SCC-I Mute	Status	
				BIT 0					BIT 0 (LSB)	
BIT 1	BIT 1									
BIT 2	BIT 2 (MSB)									
\$4B	( read / write_n )			BIT 3		Turbo MegaSD (tMSD)			Status	
				BIT 4					Turbo Pana Redirection (TPR)	Status
				BIT 5					VDP Speed Mode	0=Normal, 1=Fast
		BIT 6	Next Mapper Size	0=2048kB, 1=4096kB						
		BIT 7	Next MegaSD Mode	Status						
		\$4C	( read / write_n )	BIT 0				Turbo Pana	Status	
				BIT 1					Current Keyboard Layout	0=JP, 1=Non-JP
BIT 2	CMT (Cassette Magnetic Tape)			Status						
BIT 3	Lights Mode			0=Auto, 1=ON						
BIT 4	Red Mode (Led 0)			Status						
BIT 5	Last Reset Flag			0=Cold, 1=Warm						
BIT 6	Reset Required Flag			Status						
\$4D	( read / write_n )	BIT 7		MegaSD Blink	Status					
		BIT 0			Pseudo Stereo	Status				
		BIT 1			External Clock Mode	0=Sync to CPU, 1=3.58Mhz				
		BIT 2			Machine Type ID	BIT 0 (LSB)				
		BIT 3				BIT 1				
		BIT 4				BIT 2				
		\$4E			( read / write_n )	BIT 5		NTSC/PAL Type	BIT 3 (MSB)	
BIT 6	Forced Video Mode		0=Forced, 1=Auto							
BIT 7	0=60Hz (NTSC), 1=50Hz (PAL)									
\$4F	( read / write_n )		BIT 0			Right Inverse Audio			Status	
			BIT 1						Free	%00000000 (Empty)
			BIT 2							
			BIT 3 - 7							
		BIT 0	Free		\$FF (Null)					
		BIT 1								
		BIT 2								
BIT 3 - 7										
\$40	( read / write_n )	BIT 0		CPU Clock	Hard DIP-SW1					
		BIT 1			Video Output (MSB)	Hard DIP-SW2				
		BIT 2			Video Output (LSB)	Hard DIP-SW3				
		BIT 3			Cartridge Slot-1	Hard DIP-SW4				
		BIT 4			Cartridge Slot-2 (MSB)	Hard DIP-SW5				
		BIT 5			Cartridge Slot-2 (LSB)	Hard DIP-SW6				
		BIT 6			Internal Mapper	Hard DIP-SW7				
\$41	( read / write_n )	BIT 7		Internal MegaSD	Hard DIP-SW8					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
		\$42			( read / write_n )	BIT 3		64kB VRAM Slot ID (Page 0)	BIT 3 (MSB)	
						BIT 4			BIT 0 (LSB)	
						BIT 5			BIT 1	
BIT 6	BIT 2									
BIT 7	BIT 3 (MSB)									
\$43	( read / write_n )		BIT 0			64kB VRAM Slot ID (Page 1)			BIT 0 (LSB)	
			BIT 1						BIT 1	
		BIT 2	BIT 2							
		BIT 3	BIT 3 (MSB)							
		\$44	( read / write_n )		BIT 4			OCM-PLD version X.Y(Z) (main range 0.0 - 25.5)	BIT 0 (LSB)	
					BIT 5				BIT 1	
					BIT 6				BIT 2	
BIT 7	BIT 3 (MSB)									
\$45	( read / write_n )			BIT 0		I/O Revision ID (0 - 31)			BIT 0 (LSB)	
				BIT 1					BIT 1	
				BIT 2					BIT 2	
		BIT 3	BIT 3							
		BIT 4	BIT 4							
		BIT 5	BIT 5							
		BIT 6	BIT 6							
\$46	( read / write_n )	BIT 7		Default Keyboard Layout	BIT 7 (MSB)					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
		\$47			( read / write_n )	BIT 3		OCM-PLD version (X.Y).Z (sub range .0 - .3)	BIT 3	
						BIT 4			BIT 4 (MSB)	
						BIT 5			BIT 0 (LSB)	
BIT 6	BIT 1 (MSB)									
BIT 7	Status									
\$48	( read / write_n )		BIT 0			I/O Revision ID (0 - 31)			BIT 0 (LSB)	
			BIT 1						BIT 1	
		BIT 2	BIT 2							
		BIT 3	BIT 3							
		BIT 4	BIT 4 (MSB)							
		BIT 5	BIT 0 (LSB)							
		BIT 6	BIT 1 (MSB)							
\$49	( read / write_n )	BIT 7		Default Keyboard Layout	Status					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
		BIT 3			BIT 3					
		BIT 4			BIT 4 (MSB)					
		BIT 5			BIT 0 (LSB)					
\$4A	( read / write_n )	BIT 6		OCM-PLD version (X.Y).Z (sub range .0 - .3)	BIT 1 (MSB)					
		BIT 7			Status					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
		BIT 3			BIT 3					
		BIT 4			BIT 4 (MSB)					
\$4B	( read / write_n )	BIT 5		OCM-PLD version (X.Y).Z (sub range .0 - .3)	BIT 0 (LSB)					
		BIT 6			BIT 1 (MSB)					
		BIT 7			Status					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
		BIT 3			BIT 3					
\$4C	( read / write_n )	BIT 4		I/O Revision ID (0 - 31)	BIT 4 (MSB)					
		BIT 5			BIT 0 (LSB)					
		BIT 6			BIT 1 (MSB)					
		BIT 7			Status					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
		BIT 2			BIT 2					
\$4D	( read / write_n )	BIT 3		I/O Revision ID (0 - 31)	BIT 3					
		BIT 4			BIT 4 (MSB)					
		BIT 5			BIT 0 (LSB)					
		BIT 6			BIT 1 (MSB)					
		BIT 7			Status					
		BIT 0			BIT 0 (LSB)					
		BIT 1			BIT 1					
\$4E	( read / write_n )	BIT 2		I/O Revision ID (0 - 31)	BIT 2					
		BIT 3			BIT 3					
		BIT 4			BIT 4 (MSB)					
		BIT 5			BIT 0 (LSB)					
		BIT 6			BIT 1 (MSB)					
		BIT 7			Status					
		BIT 0			BIT 0 (LSB)					
\$4F	( read / write_n )	BIT 1		I/O Revision ID (0 - 31)	BIT 1					
		BIT 2			BIT 2					
		BIT 3			BIT 3					
		BIT 4			BIT 4 (MSB)					
		BIT 5			BIT 0 (LSB)					
		BIT 6			BIT 1 (MSB)					
		BIT 7			Status					